

(12) UK Patent Application (19) GB (11) 2 276 509 (13) A

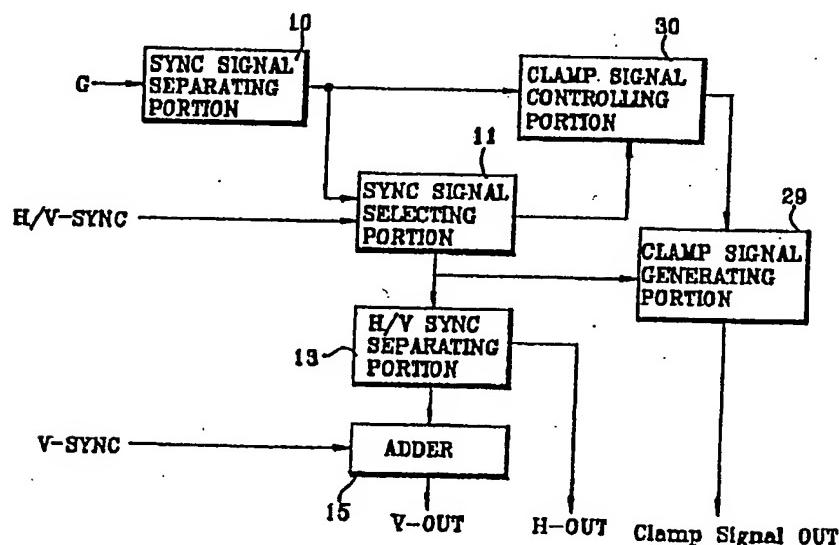
(43) Date of A Publication 28.09.1994

(21) Application No 9404750.3	(51) INT CL ⁵ H04N 5/18
(22) Date of Filing 10.03.1994	
(30) Priority Data	
(31) 933476 936852 938339 938546 938547	(32) 10.03.1993 23.04.1993 14.05.1993 19.05.1993 19.05.1993
(33) KR	
(71) Applicant(s) Samsung Electronics Co Limited	(52) UK CL (Edition M) H4F FD1B9 FD19X FD30D2 FD30H FD73X FD79 FEA
(Incorporated in the Republic of Korea) 416 Maetan-dong, Paldal-gu, Suwon-City, Kyonggi-do, Republic of Korea	(56) Documents Cited JP 050244450 A US 5008753 A
(72) Inventor(s) Tae Jin Park	(58) Field of Search UK CL (Edition M) H4F FEA FED FEF INT CL ⁵ H04N 5/05 5/18 Online databases: WPI Inspec.
	(72) cont Ho Dae Hwang Joong Yeol Kwon
	(74) Agent and/or Address for Service J Miller & Co 34 Bedford Row, Holborn, LONDON, WC1R 4JH, United Kingdom

(54) Video clamp signal generation control circuit discriminates source of sync pulses

(57) A clamp signal generation-control circuit includes a clamp signal controller 30 for discriminating between a sync on green signal and a separate sync signal supplied from a video card to a monitor and outputs a control signal according to the discrimination. A clamp signal generator 29 automatically changes a generated point of the clamp signal in accordance with the discriminator output. The generated point of the clamp signal supplied to the monitor is controlled to automatically shift the clamping position of a video signal, thereby preventing malfunction caused by a nonprofessional user's manipulation, abnormal picture display or the generation of a segment phenomenon. The circuit is suitable for monitors supplied with various kinds of sync signals.

FIG.6



GB 2 276 509 A

FIG.1

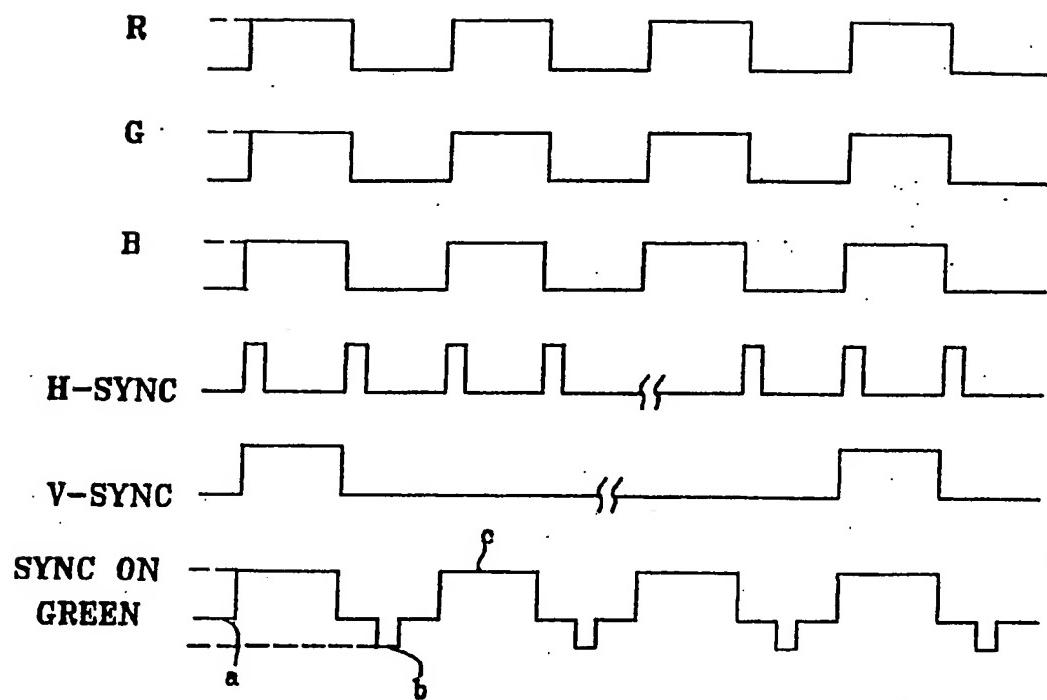


FIG.2

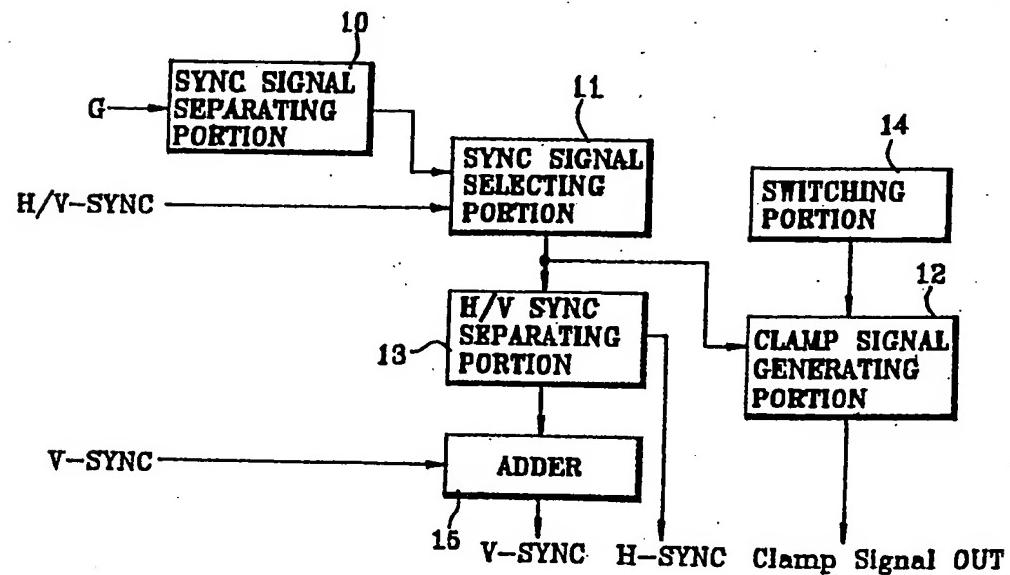


FIG. 3

2 / 11

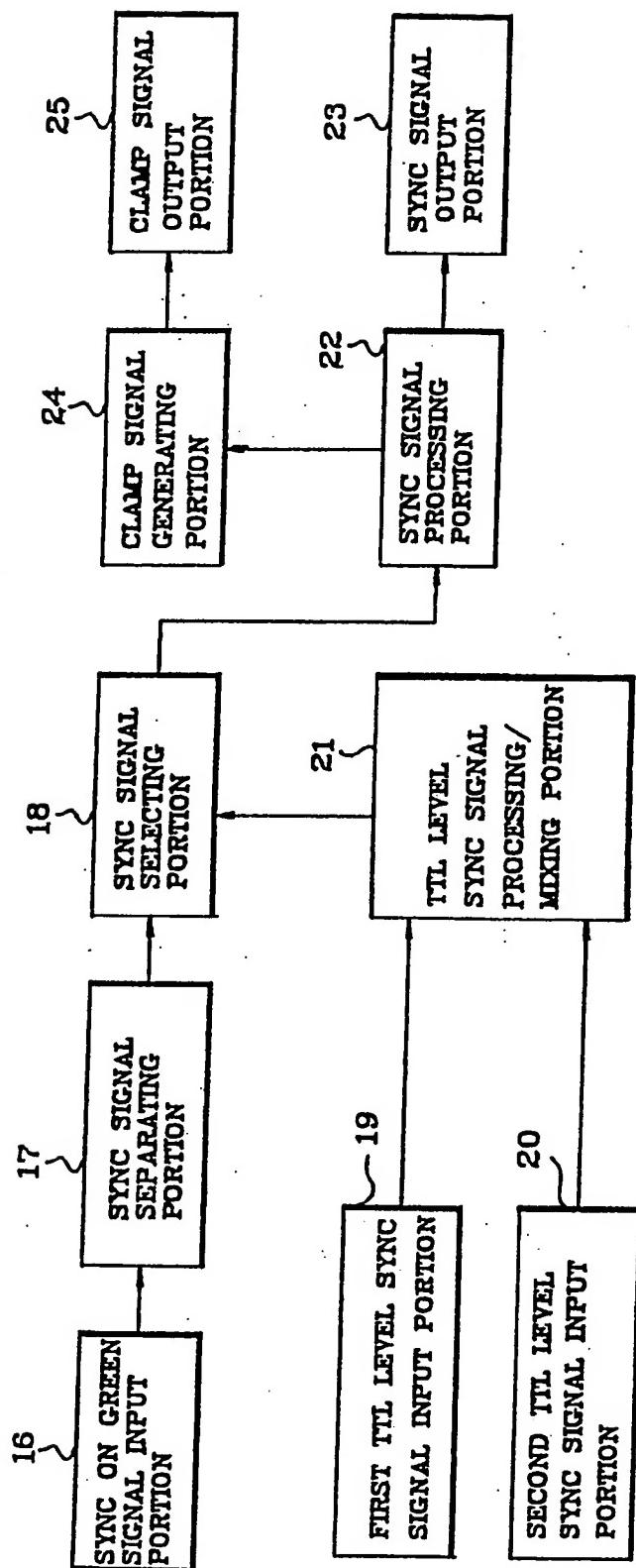


FIG.4

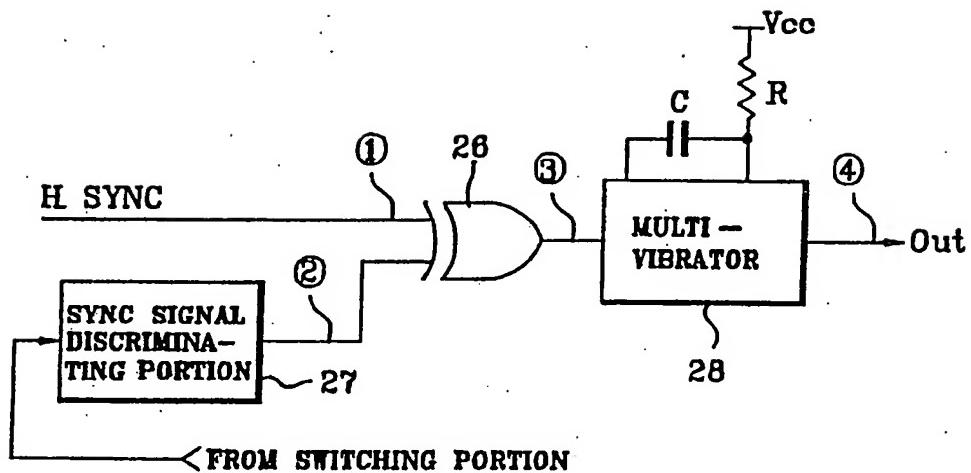


FIG.5

	CLASSIFICATION	①	②	③	①, ④
(A)	SYNC ON GREEN		LOW		
(B)	SEPARATING SYNC		HIGH		

FIG.6

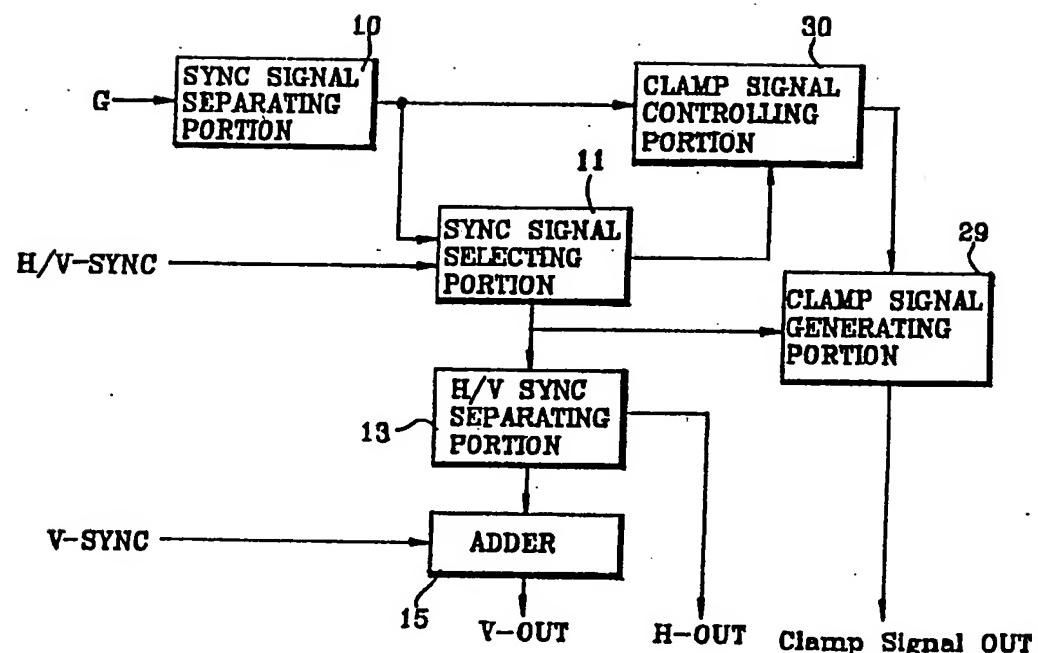
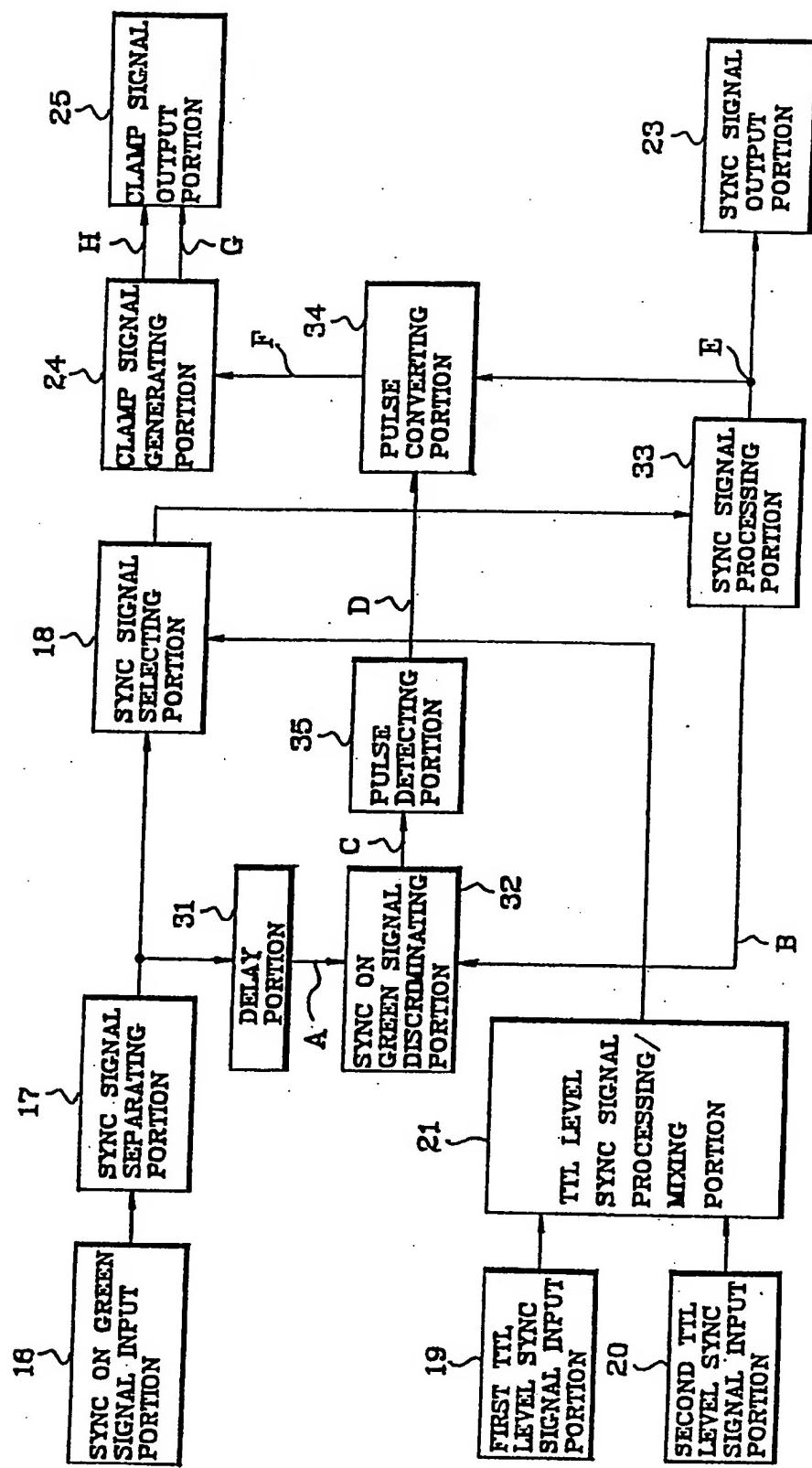
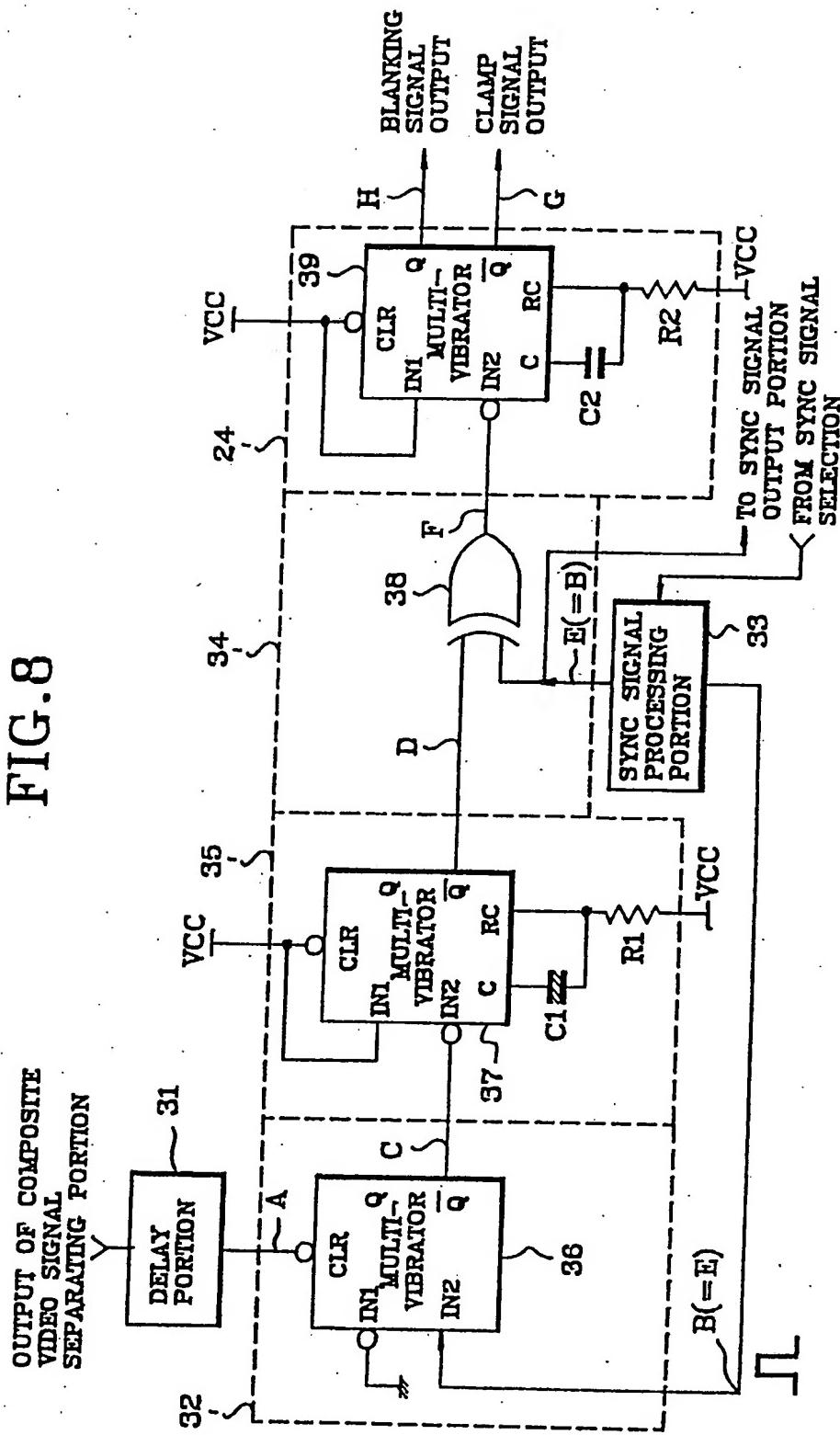


FIG. 7



6 / 11

FIG. 8



**OUTPUT OF COMPOSITE
SEPARATING PORTION
VIDEO SIGNAL**

APX 312912

FIG.9

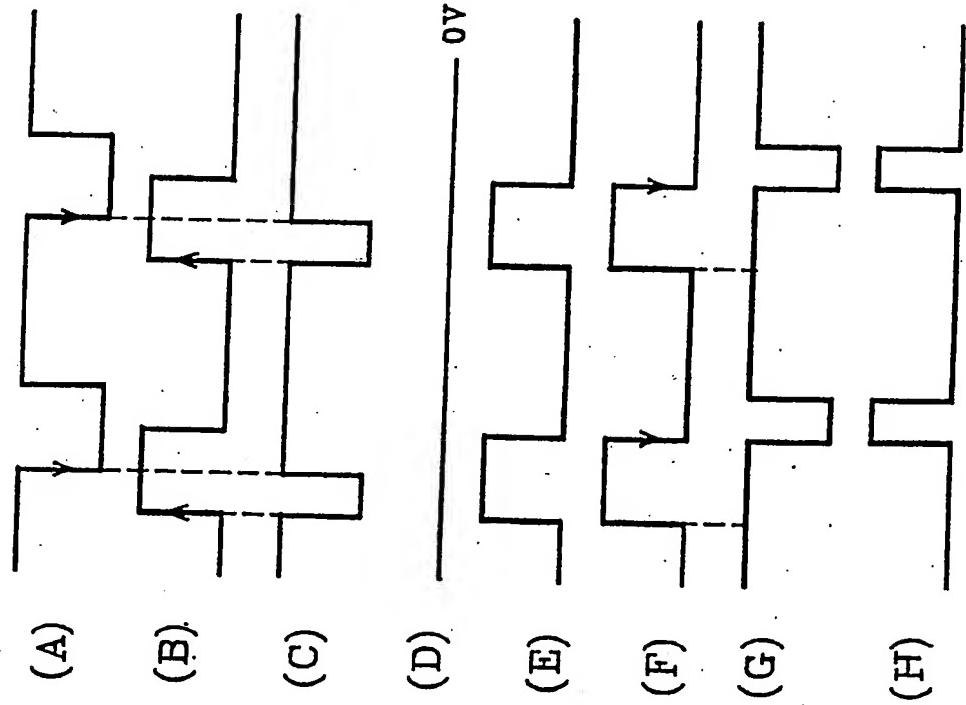
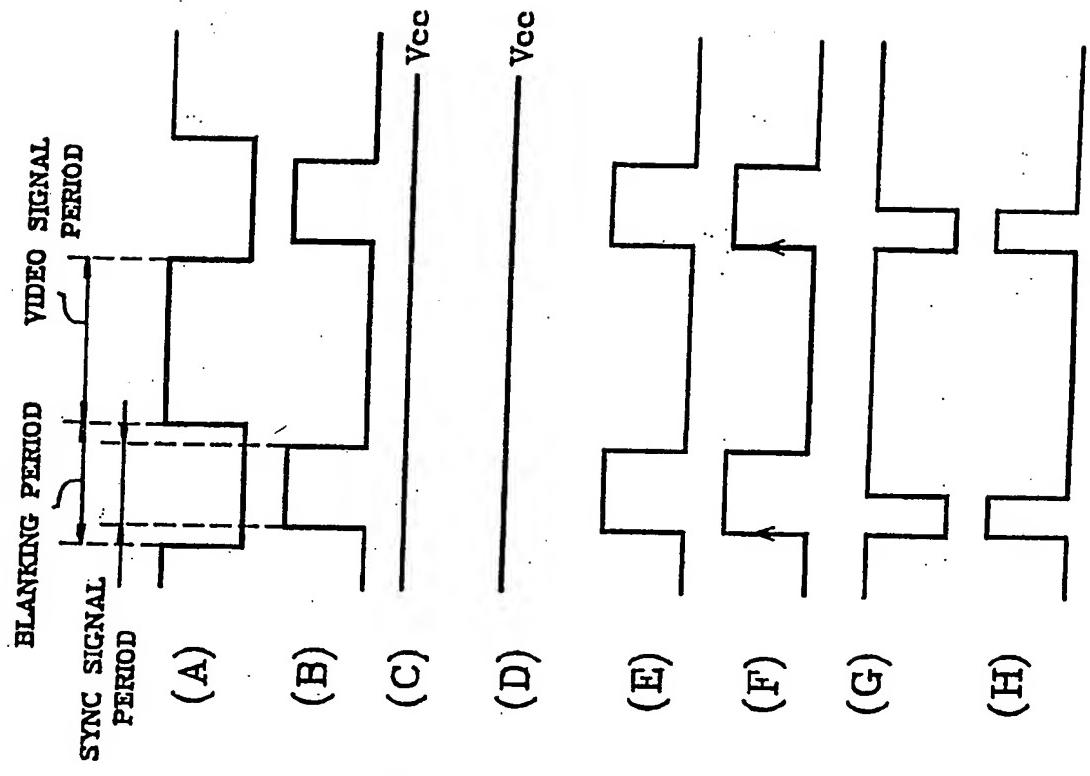


FIG.10



7/11

FIG.11

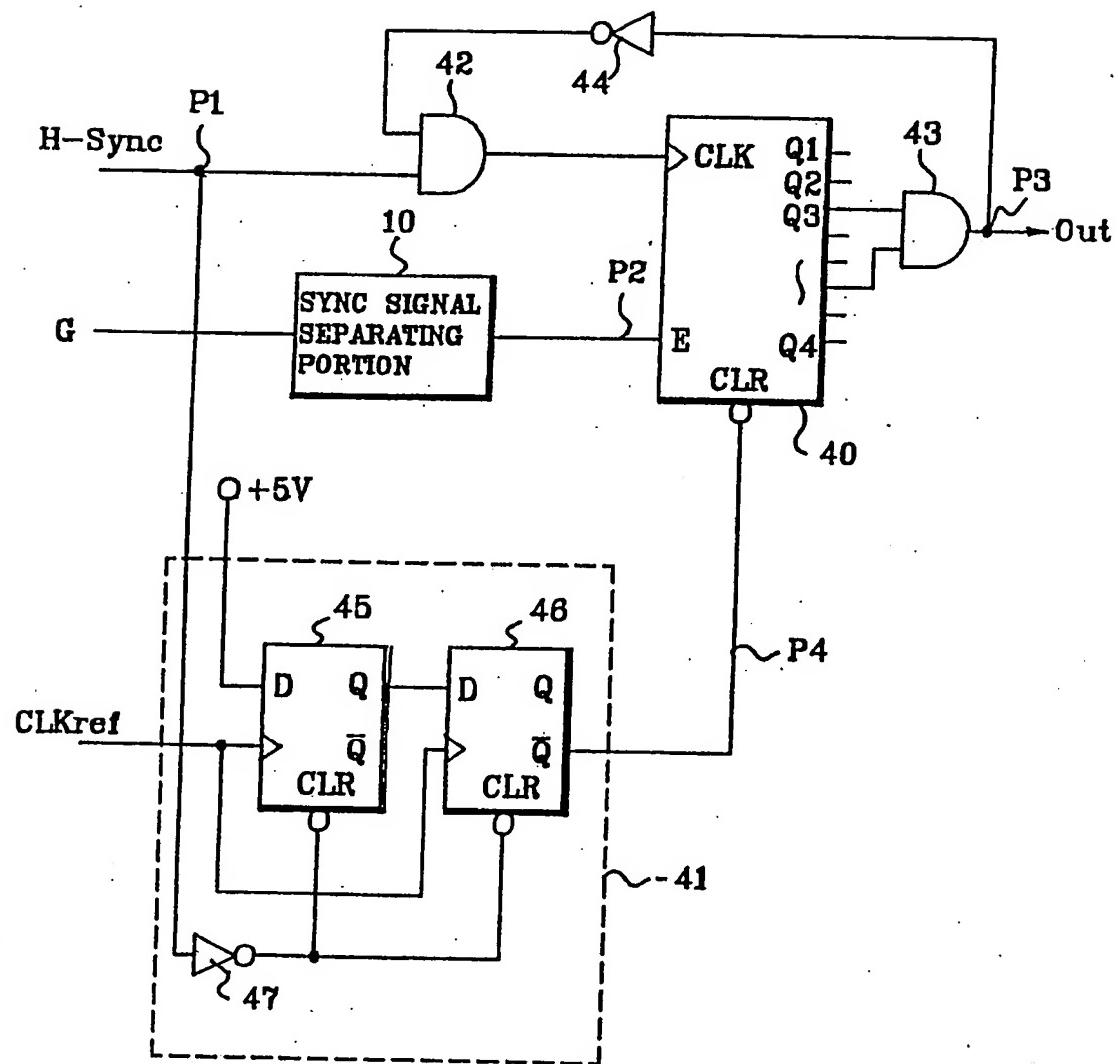


FIG.12A

FIG.12B

FIG.12C

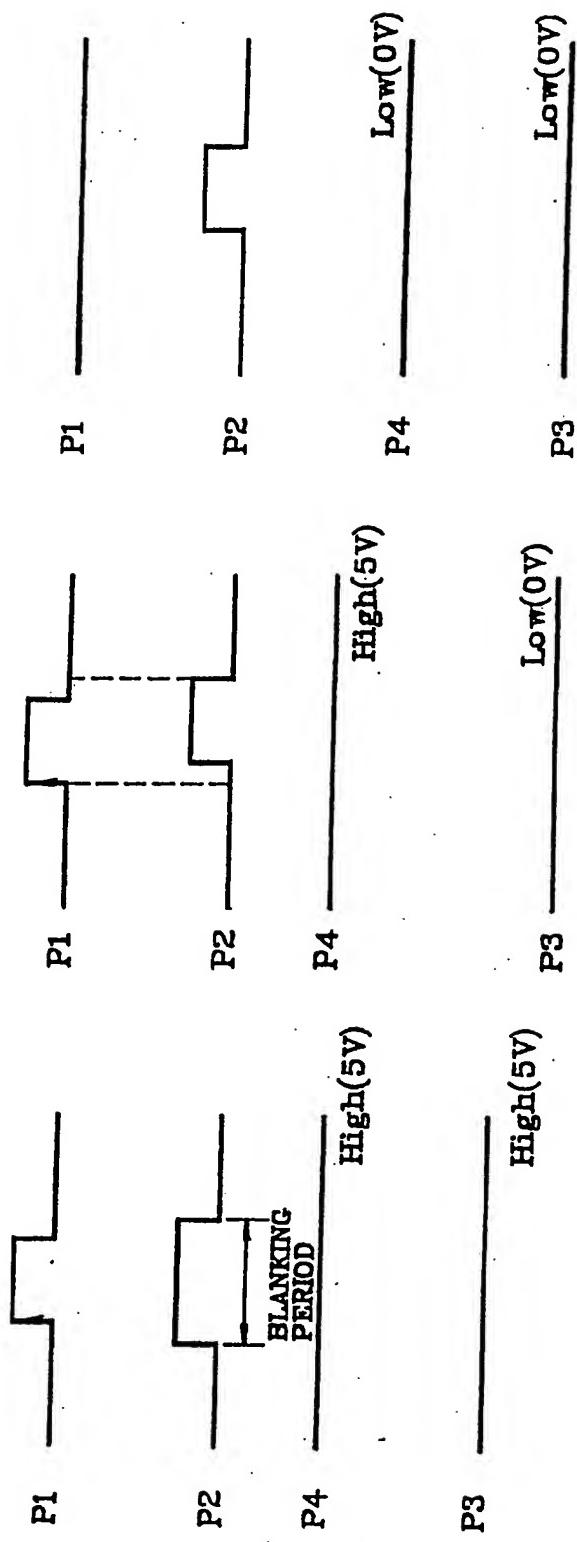
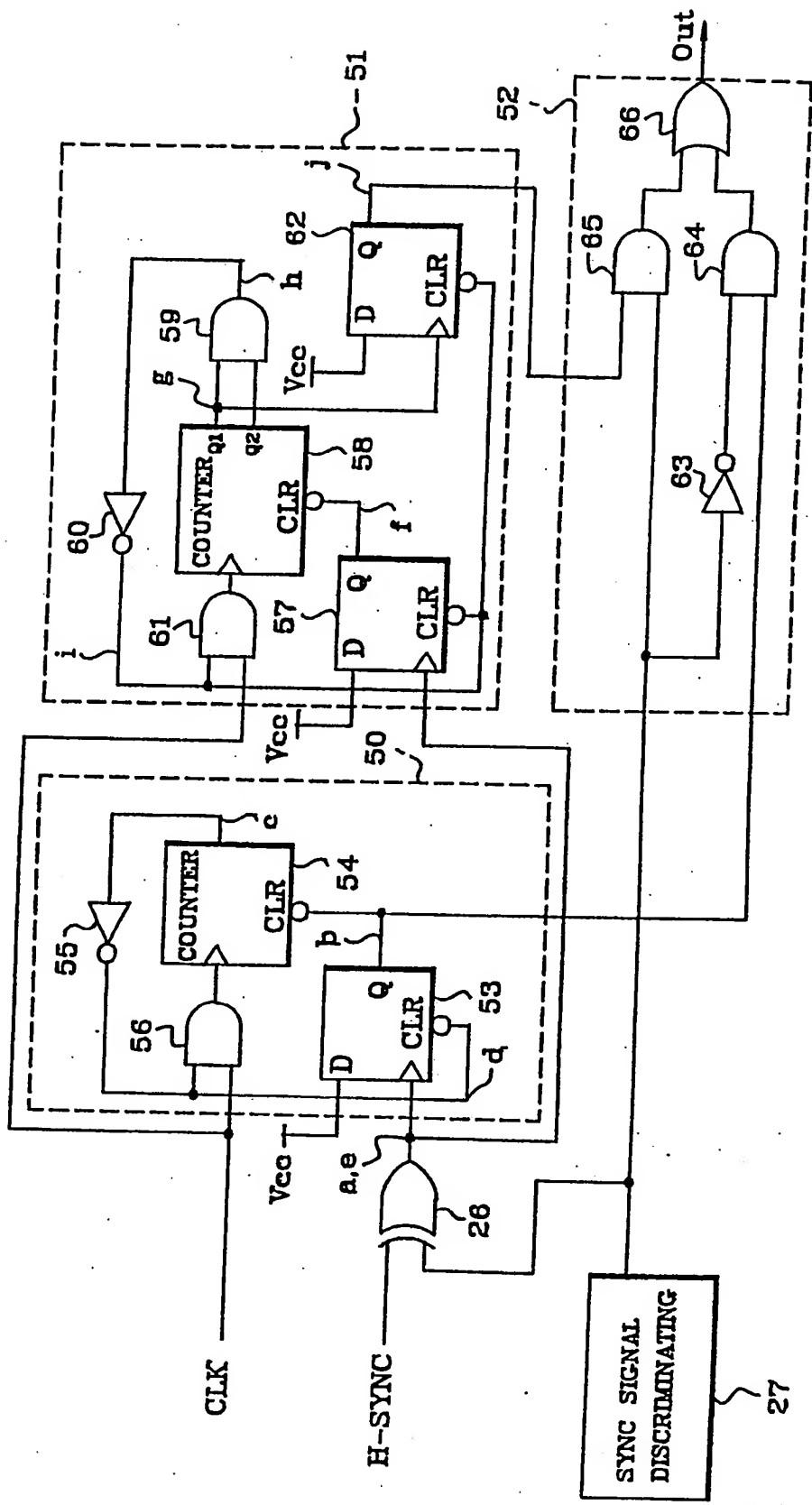


FIG.13



10/11

27

FIG.14

